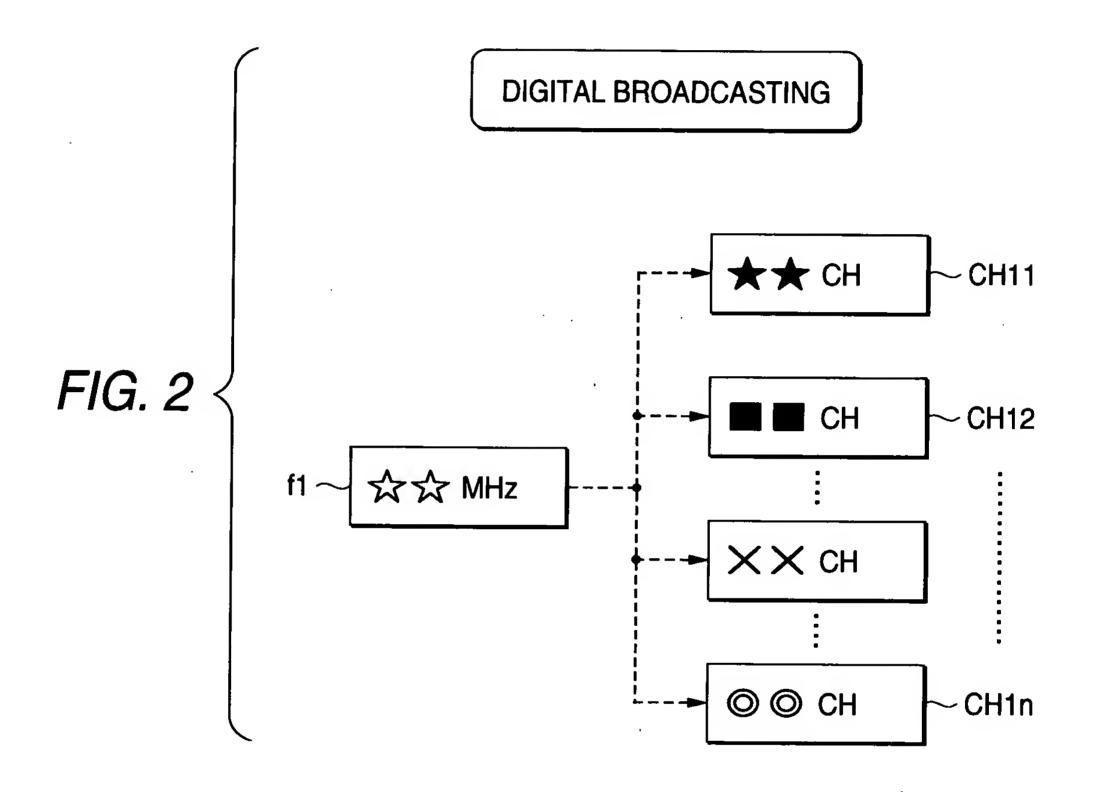
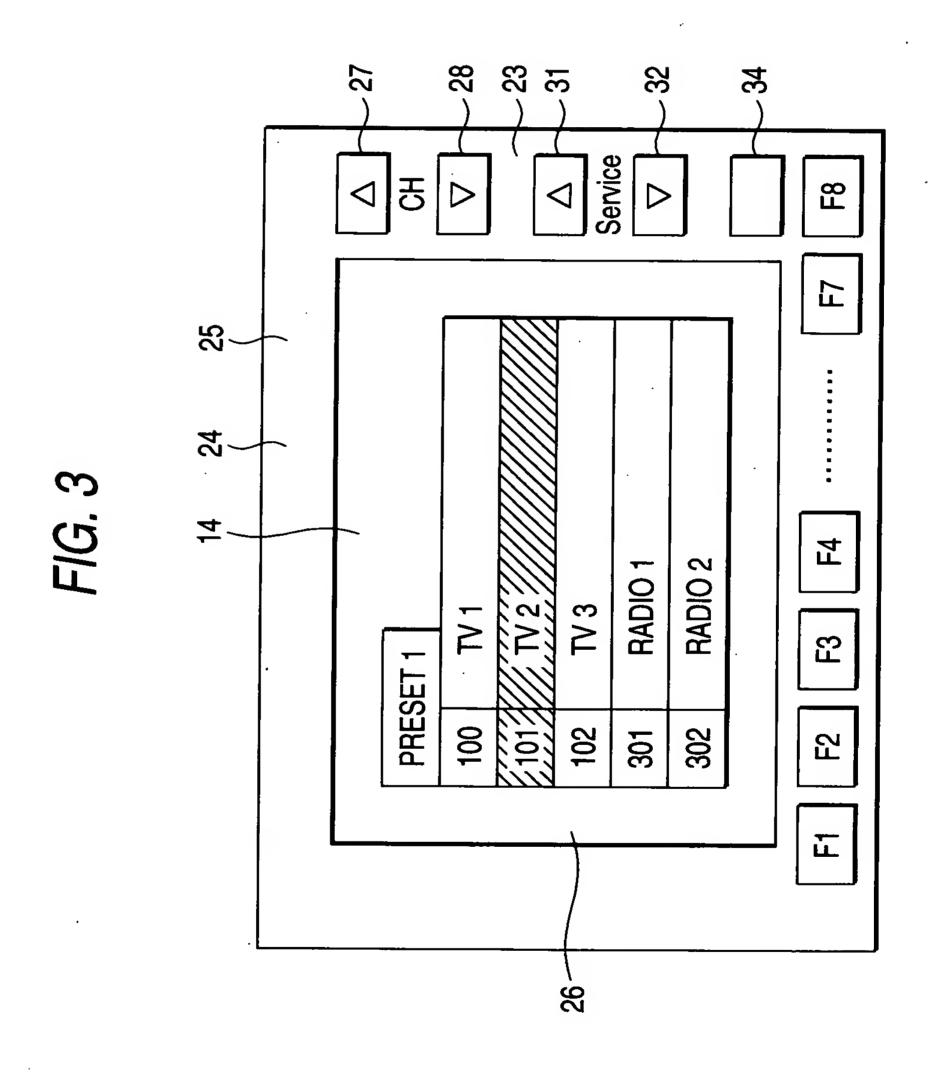
FIG. 1

OPERATION INPUT UNIT ~20 8, PRESENTATION PROCESSING CIRCUIT 5, **o**, ∞ MODEM SOUND DECODING UNIT VIDEO DECODING UNIT DIGITAL BROADCAST RECEIVING APPARATUS 9 CPU TS DECODING V Ω· ROM DE-MODULATION CIRCUIT 16 RAM -21 9 TUNER





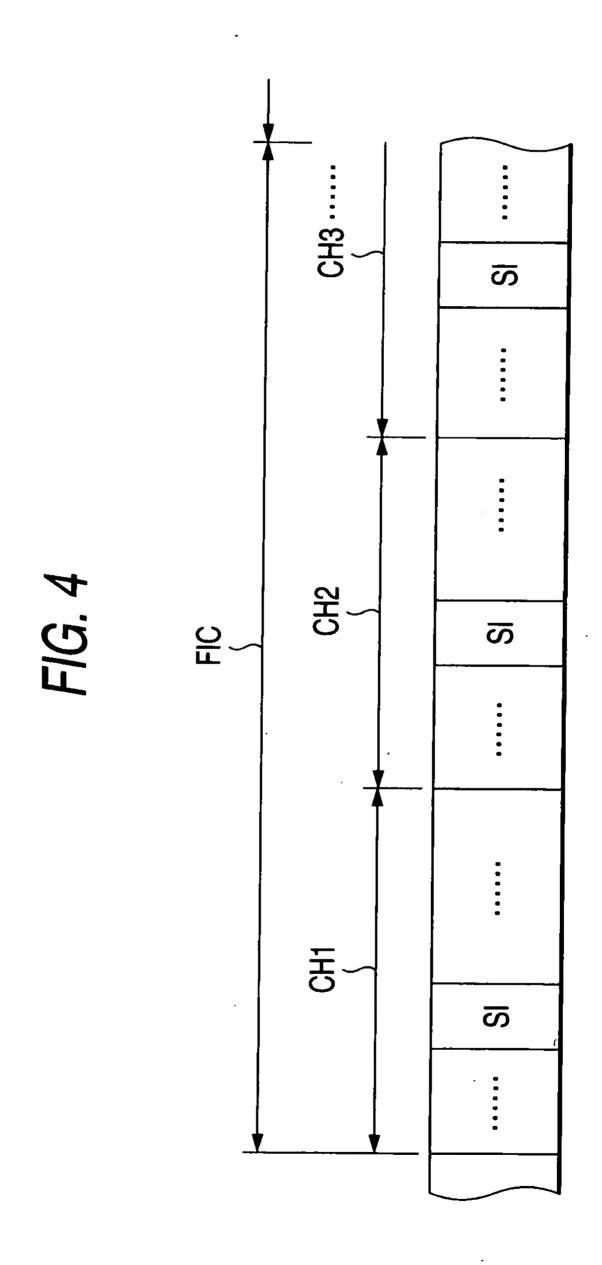


FIG. 5

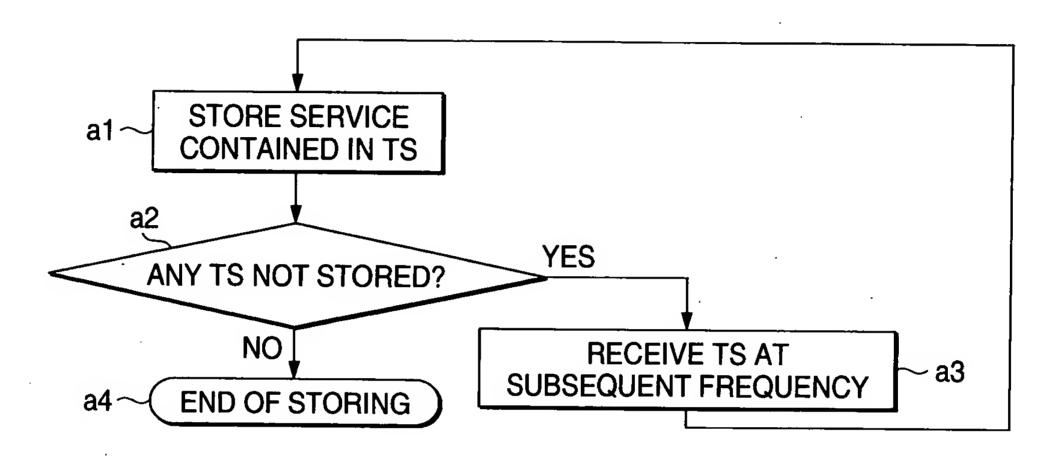


FIG. 6

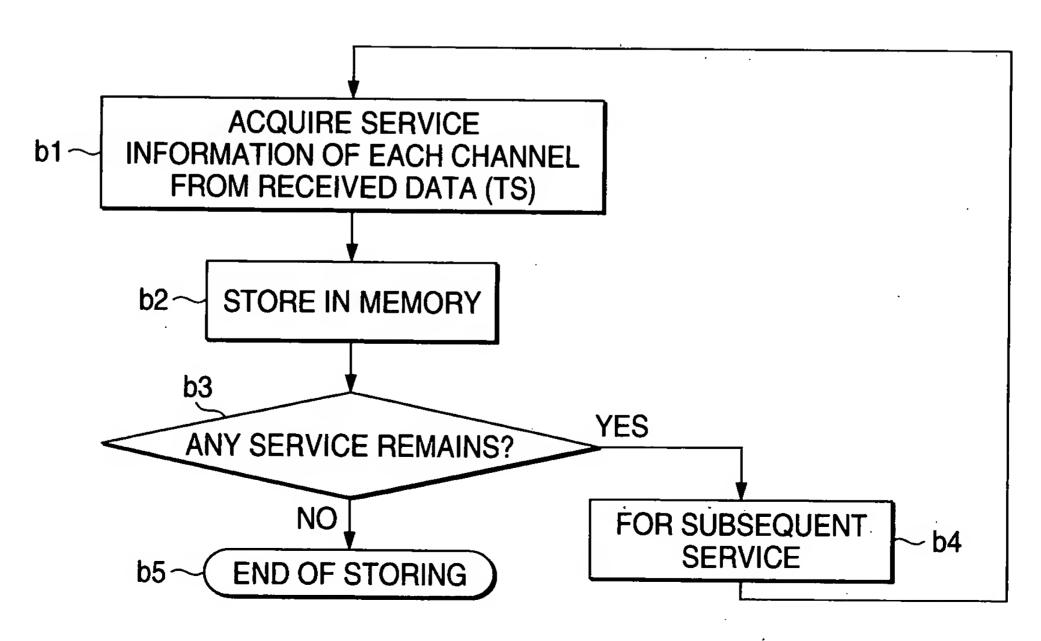


FIG. 7

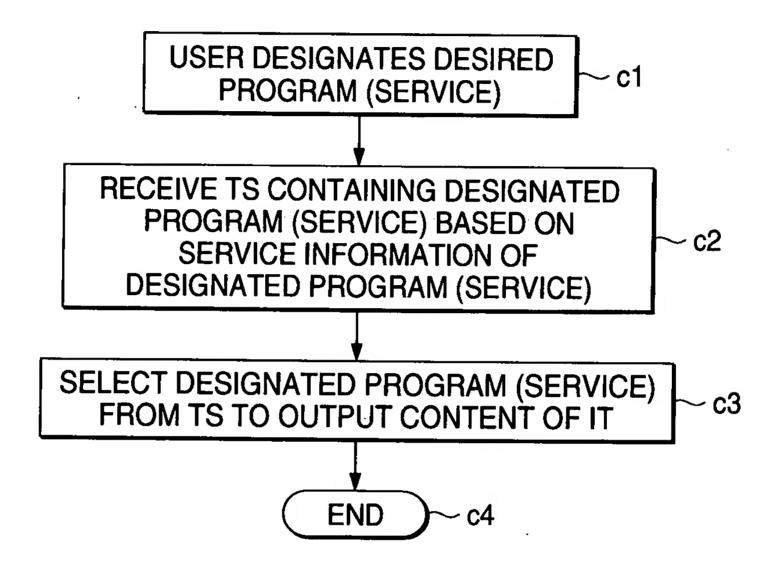


FIG. 8

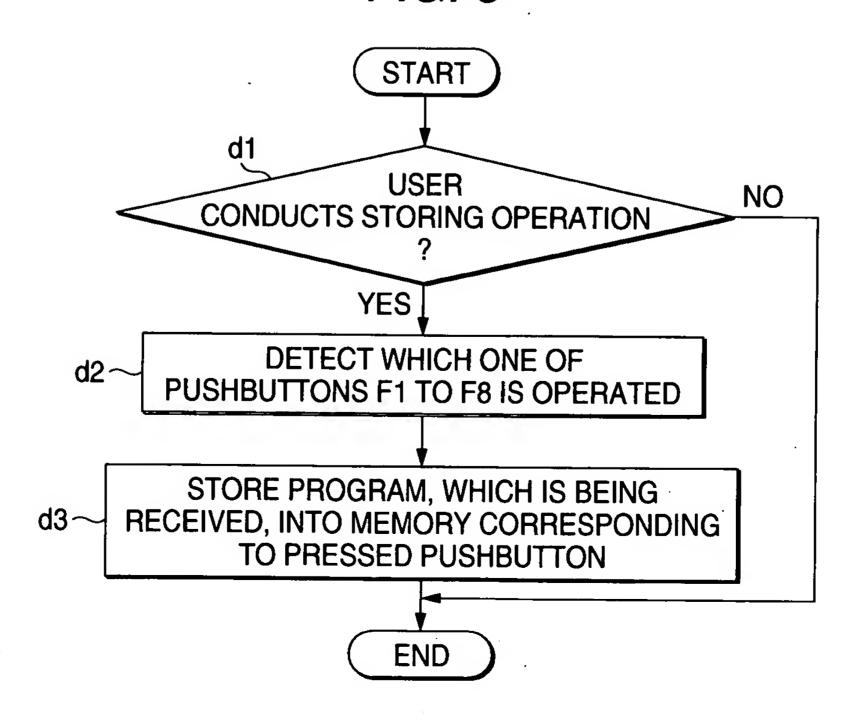
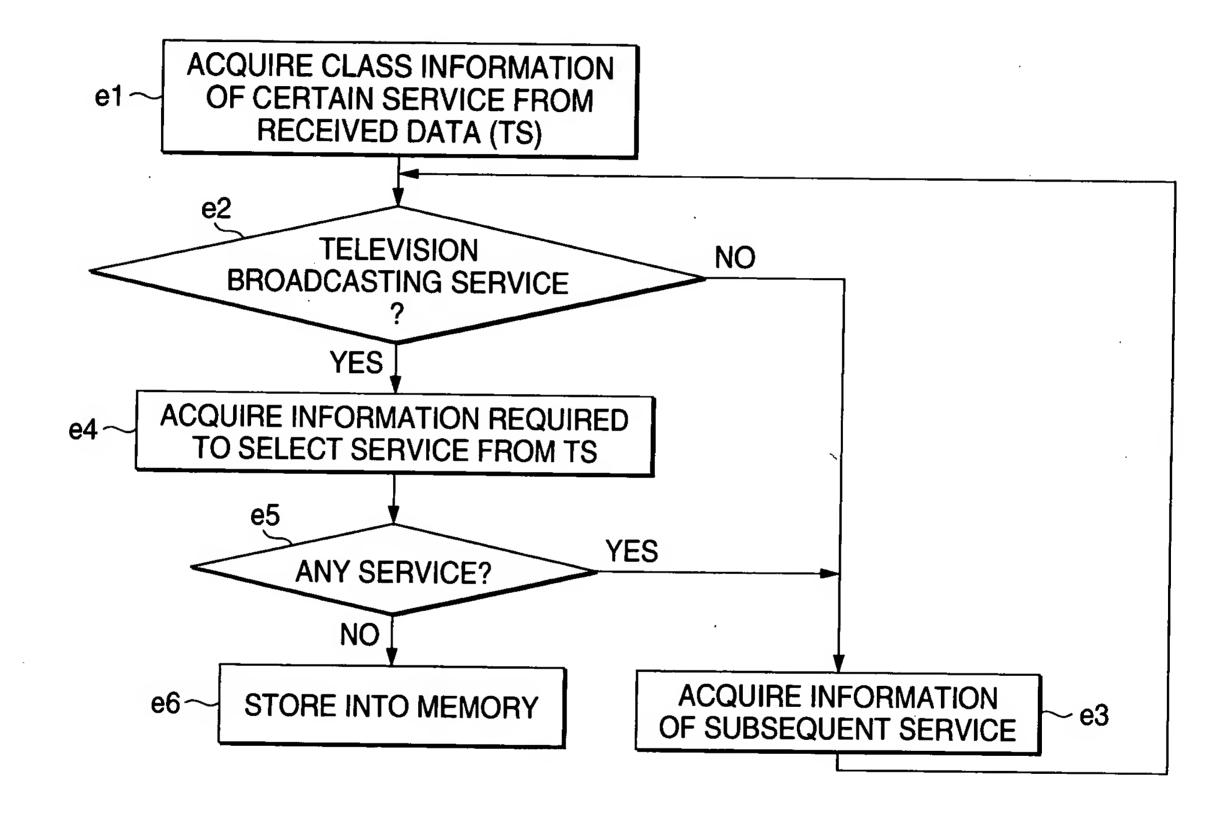


FIG. 9



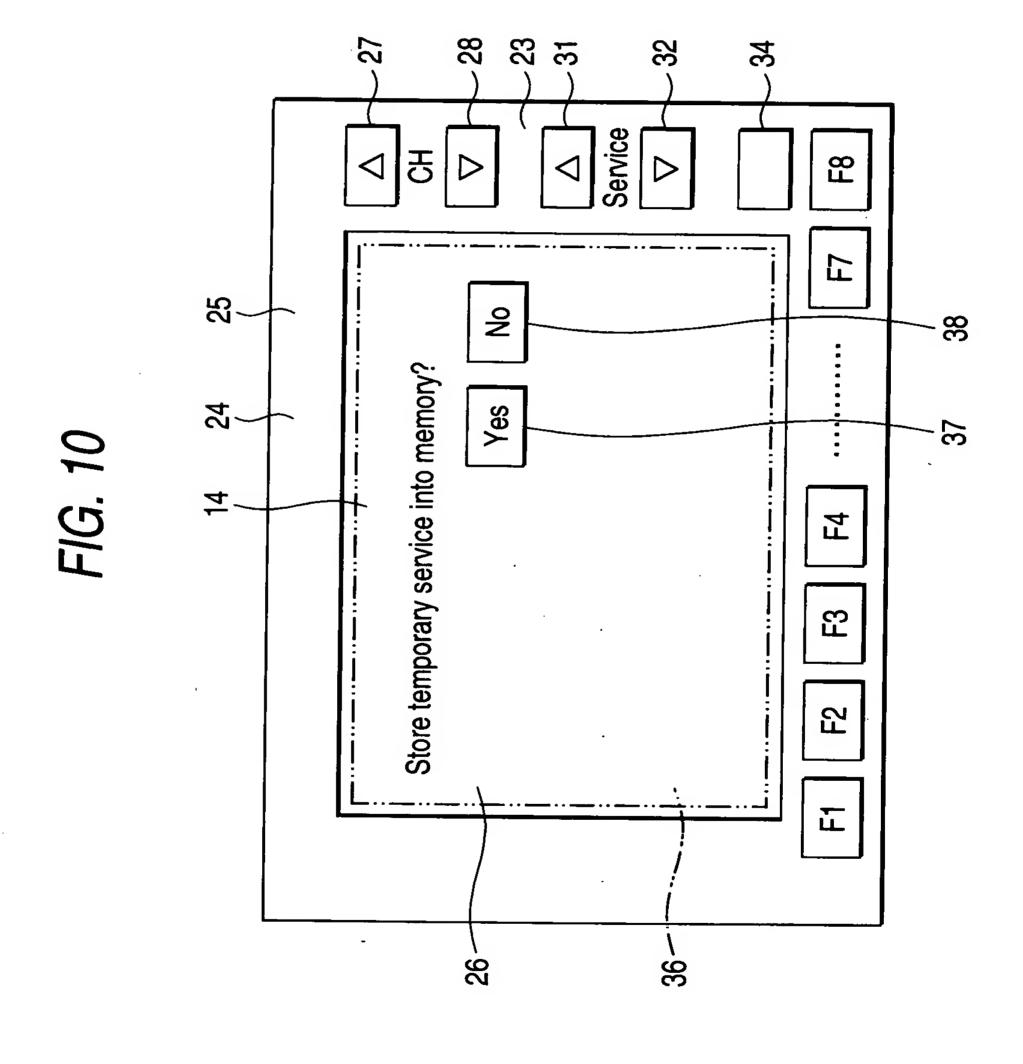


FIG. 11

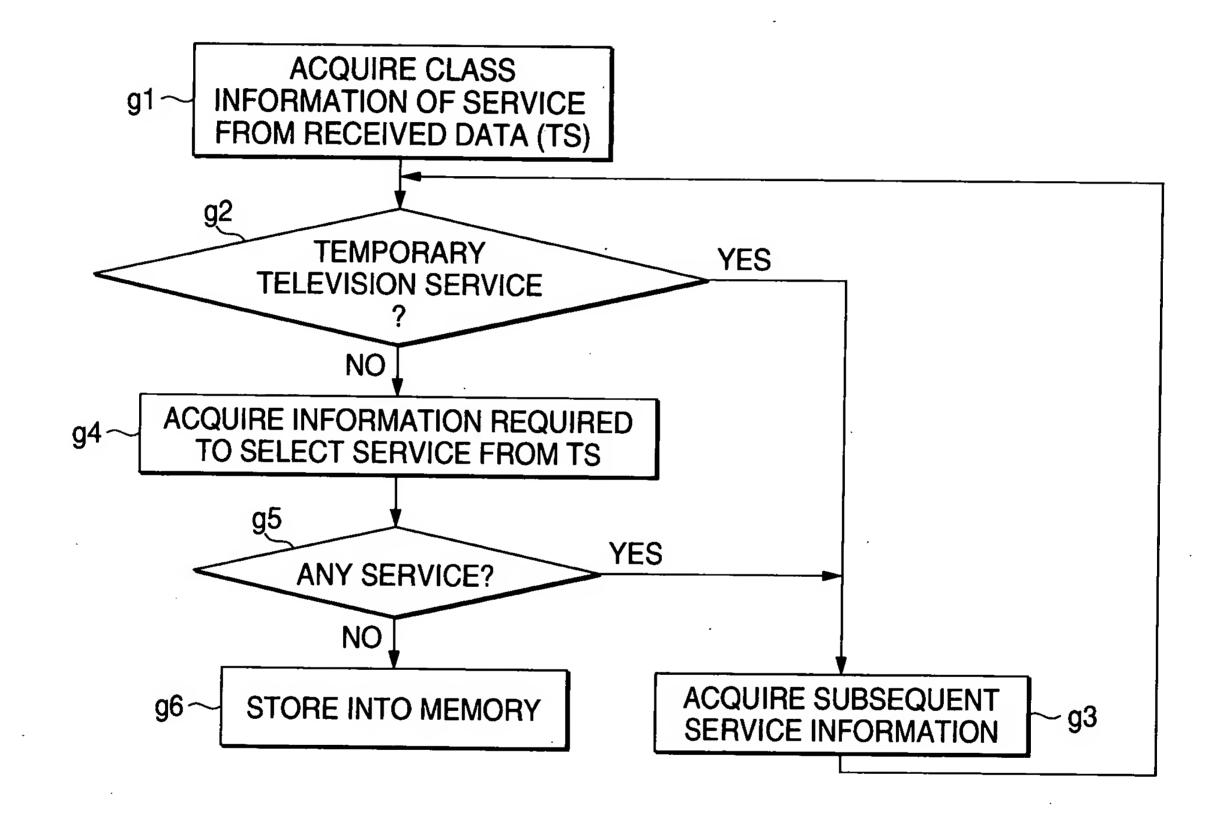


FIG. 12

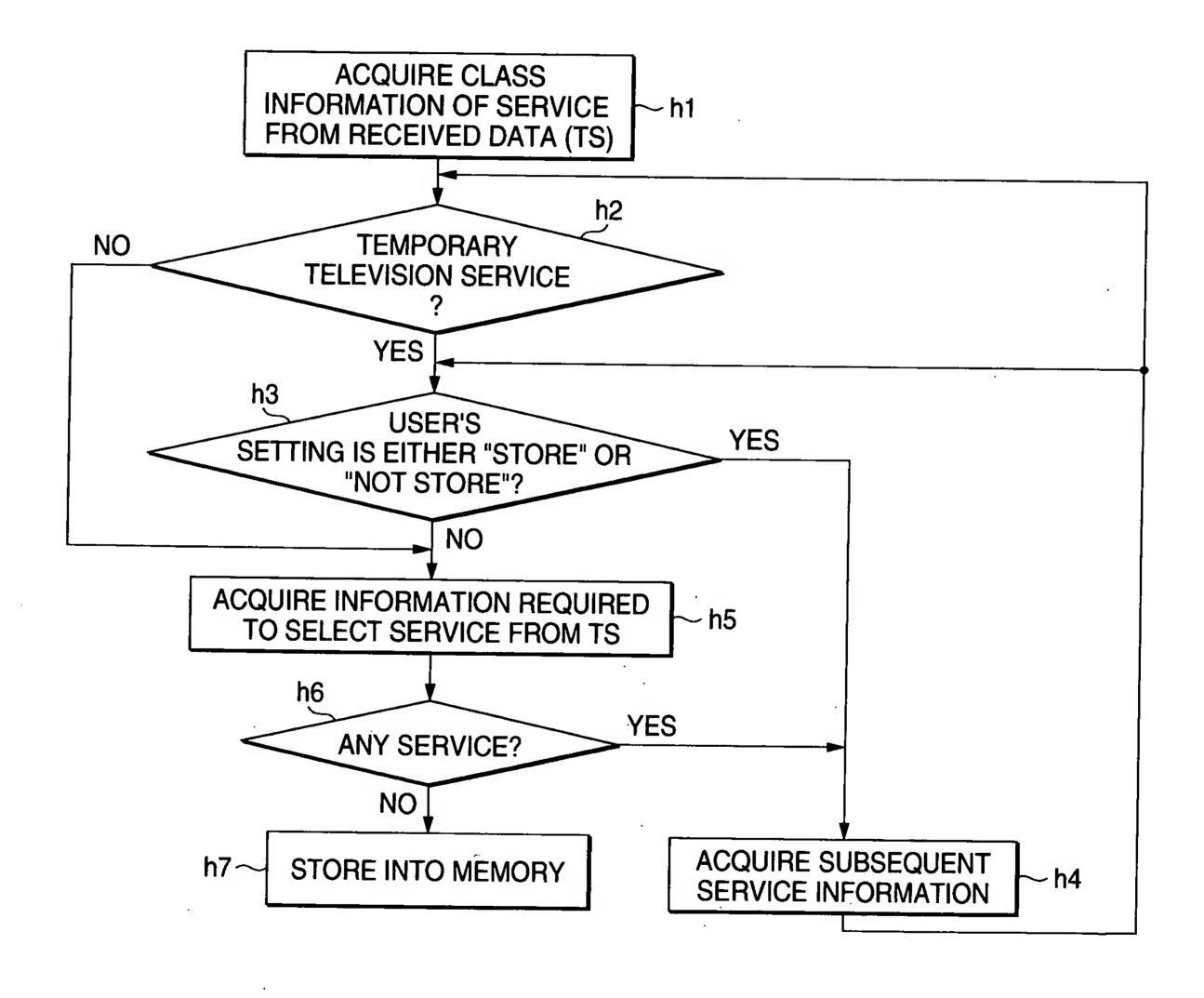


FIG. 13

